



REPLY UNDER 37 C.F.R. § 1.116 EXPEDITED PROCEDURE EXAMINING GROUP 2186

PATENT 5681-05200/P6799

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

10/027,359

Filed:

December 19, 2001

Inventors:

Fay Chong, Jr.

Title:

Cache Accumulator

Memory for Performing

Operations on Block

Operands

Examiner:

Tsai, Sheng Jen

Group/Art Unit:

2186

Atty. Dkt. No: 5681-05200

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert

Printed Name

August 21, 2006

RESPONSE TO FINAL ACTION OF JUNE 20, 2006

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Please do NOT enter. Thanks. 8/28/2006

This paper is submitted in response to the Final Action of June 20, 2006, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.